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AMENDMENTS TO THE SPECIFICATION

Kindly amend the above-identified application as follows:

In the Specification:

Please replace the first four lines of paragraph 53 on page 17, as follows:

--Turning Now to FIG. 6, there is shown a block diagram illustration of one possible circuit configuration associated with a NVM array for performing a check[[s]] sum based error detection algorithm and for selecting a set of reference cells based on the results of the check sum algorithm.--

Please replace the first sentence of paragraph 56 on page 18, as follows:

-- The Read Verify ~~reverence~~ reference threshold voltage associated with each program state may be adjusted in response to an error detected in reading during step 610 (block 620).--